



Docket No.: M4065.0411/P411
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Roy Greeff et al.

Allowed: November 1, 2004

Application No.: 09/893,616

Confirmation No.: 5708

Filed: June 29, 2001

Art Unit: 2111

For: DATA TRANSMISSION CIRCUIT
FOR MEMORY SUBSYSTEM, HAS
SWITCHING CIRCUIT THAT
SELECTIVELY CONNECTS OR
DISCONNECTS TWO DATA BUS
SEGMENTS TO RESPECTIVELY
ENABLE DATA TRANSMISSION OR
I/O CIRCUIT CONNECTION

Examiner: R. N. Phan

**COMMENTS ON EXAMINER'S
STATEMENT OF REASONS FOR ALLOWANCE**

U.S. Patent and Trademark Office
220 20th Street S.
Customer Window, Mail Stop Issue Fee
Crystal Plaza Two, Lobby, Room 1B03
Arlington, VA 22202

Dear Sir:

Applicants agree in part with the Examiner's statement for allowance in that the prior art of record fails to disclose the limitations noted by the Examiner. However, the Examiner's statement does not reflect all of the language of each of the allowed claims. The totality of the language of each claim provides additional reasons for the allowance of each claim.

• Dated: January 28, 2005

Respectfully submitted,

By 

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